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Docket No.: 060188-0075

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
Makoto FUJIWARA	:	Confirmation Number: 5700
Application No.: 09/867,766	:	Group Art Unit: 2134
Filed: May 31, 2001	:	Examiner: Poltorak, Piotr
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING SEMICONDUCTOR INTEGRATED CIRCUIT		

PETITION FOR EXTENSION OF TIME

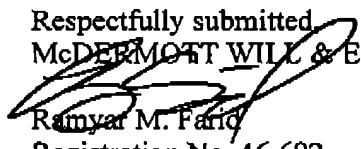
Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the time for response to the Office Action dated January 13, 2006, now due to expire April 13, 2006, be extended for one month(s) and set to expire on May 13, 2006 (Saturday).

Please charge the extension fee of \$120.00 to Deposit Account No. 500417. Please charge any additional fees or credit any overpayment to Deposit Account No. 500417.

Respectfully submitted
McDERMOTT WILL & EMERY LLP


Ramyar M. Farid
Registration No. 46,692

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 RMF:men
Facsimile: 202.756.8087
Date: May 15, 2006

Please recognize our Customer No. 20277
as our correspondence address.

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